

Electronic Acknowledgement Receipt

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Application Number:	10083533
International Application Number:	
Confirmation Number:	6400
Title of Invention:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD CHARACTERISTIC
First Named Inventor/Applicant Name:	Hiroshi Hashimoto
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed (SB/08)	020244_IDS_Form_SB08_due_02_20_09.pdf	764430 f65d535235fd52501b224bc12dfe1d7fb037 95f2	no	4

Warnings:

Information:

2	NPL Documents	020244_European_OA.pdf	272338 7e39dc92014c8decc485154c24f9db906711 f1b3	no	3
Warnings:					
Information:					
Total Files Size (in bytes):			1036768		
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